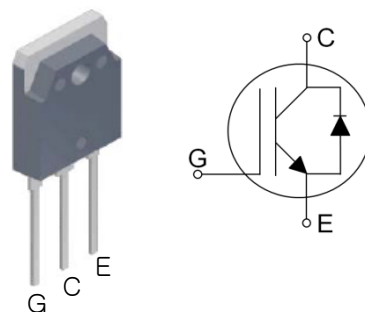


Features

- 1350V Reverse Conducting Field Stop Trench IGBT Technology
- Excellent EMI Behavior
- High Speed Switching
- Low Conduction Loss
- Positive Temperature Coefficient
- Easy Parallel Operation
- 175°C Operating Temperature
- RoHS Compliant
- JEDEC Qualification



Applications

- Induction Heating
- Inverterized microwave ovens
- Soft Switching Applications

Device	Package	Marking	Remark
TGAN20S135FD	TO-3PN	TGAN20S135FD	RoHS

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CES}	1350	V
Gate-Emitter Voltage	V_{GES}	± 25	V
Continuous Collector Current	I_C	$T_C = 25\text{ }^\circ\text{C}$	40
		$T_C = 100\text{ }^\circ\text{C}$	20
Pulsed Collector Current (Note 1)	I_{CM}	100	A
Diode Continuous Forward Current	I_F	20	A
Power Dissipation	P_D	$T_C = 25\text{ }^\circ\text{C}$	242
		$T_C = 100\text{ }^\circ\text{C}$	121
Operating Junction Temperature	T_{vj}	-55 ~ 175	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ 150	$^\circ\text{C}$
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	T_L	300	$^\circ\text{C}$

Notes :
 (1) Repetitive rating : Pulse width limited by maximum junction temperature , During production, high current switching capability is 100% verified with the inductive load single-pulse switching test. ($I_C=100A$)

Thermal Characteristics

Parameter	Symbol	Value	Unit
Maximum Thermal resistance, Junction-to-Case	$R_{\theta JC}$ (IGBT)	0.62	$^\circ\text{C/W}$
Maximum Thermal resistance, Junction-to-Case	$R_{\theta JC}$ (DIODE)	0.62	$^\circ\text{C/W}$
Maximum Thermal resistance, Junction-to-Ambient	$R_{\theta JA}$	40	$^\circ\text{C/W}$

Electrical Characteristics $T_{vj}=25^{\circ}\text{C}$, unless otherwise noted

Parameter	Symbol	Test condition	Min.	Typ.	Max.	Unit
OFF						
Collector – Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0V, I_C = 1mA$	1350	--	--	V
Zero Gate Voltage Collector Current	I_{CES}	$V_{CE} = 1350V, V_{GE} = 0V$	--	--	1	mA
Gate – Emitter Leakage Current	I_{GES}	$V_{CE} = 0V, V_{GE} = \pm 25V$	--	--	± 500	nA
Integrated Gate Resistor	$R_{G(int)}$	$f = 1MHz, \text{open Collector}$	--	21	--	Ω
ON						
Gate – Emitter Threshold Voltage	$V_{GE(TH)}$	$V_{GE} = V_{CE}, I_C = 20mA$	4.0	6.0	8.0	V
Collector – Emitter Saturation Voltage	$V_{CE(SAT)}$	$V_{GE} = 15V, I_C = 20A, T_{vj} = 25^{\circ}\text{C}$	--	1.75	2.15	V
		$V_{GE} = 15V, I_C = 20A, T_{vj} = 125^{\circ}\text{C}$	--	2.04	--	V
		$V_{GE} = 15V, I_C = 20A, T_{vj} = 175^{\circ}\text{C}$	--	2.23	--	V
Diode Forward Voltage	V_{FM}	$I_F = 20A, T_{vj} = 25^{\circ}\text{C}$	--	2.25	--	V
		$I_F = 20A, T_{vj} = 125^{\circ}\text{C}$	--	2.43	--	V
		$I_F = 20A, T_{vj} = 175^{\circ}\text{C}$	--	2.53	--	V
DYNAMIC						
Input Capacitance	C_{IES}	$V_{CE} = 30V,$ $V_{GE} = 0V$ $f = 1MHz$	--	2036	--	pF
Output Capacitance	C_{OES}		--	42	--	pF
Reverse Transfer Capacitance	C_{RES}		--	30	--	pF
Total Gate Charge	Q_g	$V_{CC} = 600V, I_C = 20A$ $V_{GE} = 15V$	--	115	172	nC
Gate-Emitter Charge	Q_{ge}		--	17	25	nC
Gate-Collector Charge	Q_{gc}		--	46	69	nC

Parameter	Symbol	Test condition	Min.	Typ.	Max.	Unit
SWITCHING (Note 2)						
Turn-Off Delay Time	$t_{d(off)}$	$V_{CC} = 600V, I_C = 10A$ $R_G = R_{G(int)}, V_{GE} = 15V$ Inductive Load, $T_{vj} = 25\text{ }^\circ\text{C}$	--	142	--	ns
Fall Time	t_f		--	106	159	ns
Turn-Off Switching Loss	E_{OFF}		--	0.18	0.27	mJ
Turn-Off Delay Time	$t_{d(off)}$	$V_{CC} = 600V, I_C = 20A$ $R_G = R_{G(int)}, V_{GE} = 15V$ Inductive Load, $T_{vj} = 25\text{ }^\circ\text{C}$	--	142	--	ns
Fall Time	t_f		--	93	140	ns
Turn-Off Switching Loss	E_{OFF}		--	0.50	0.75	mJ
Turn-Off Delay Time	$t_{d(off)}$	$V_{CC} = 600V, I_C = 10A$ $R_G = R_{G(int)}, V_{GE} = 15V$ Inductive Load, $T_{vj} = 175\text{ }^\circ\text{C}$	--	173	--	ns
Fall Time	t_f		--	191	--	ns
Turn-Off Switching Loss	E_{OFF}		--	0.34	0.51	mJ
Turn-Off Delay Time	$t_{d(off)}$	$V_{CC} = 600V, I_C = 20A$ $R_G = R_{G(int)}, V_{GE} = 15V$ Inductive Load, $T_{vj} = 175\text{ }^\circ\text{C}$	--	169	--	ns
Fall Time	t_f		--	186	--	ns
Turn-Off Switching Loss	E_{OFF}		--	0.89	1.34	mJ

Notes :

(2) Not subject to production test – verified by design/characterization

Device Characteristics

Fig. 1 IGBT Output Characteristics

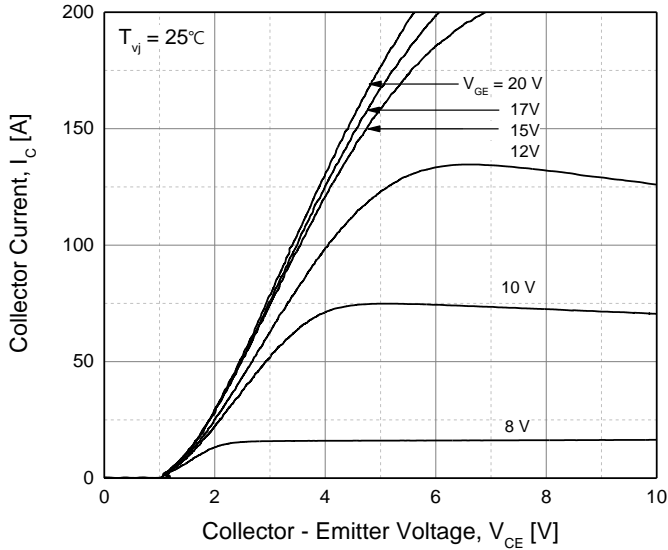


Fig. 2 IGBT Output Characteristics

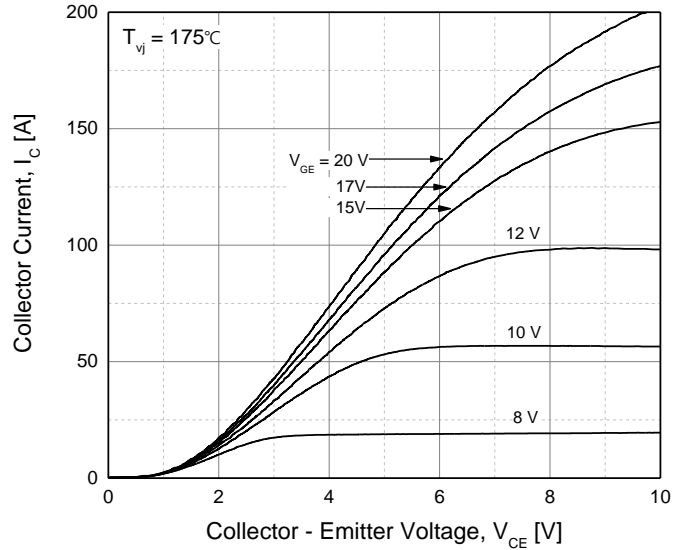


Fig. 3 IGBT Saturation Voltage vs. Junction Temperature

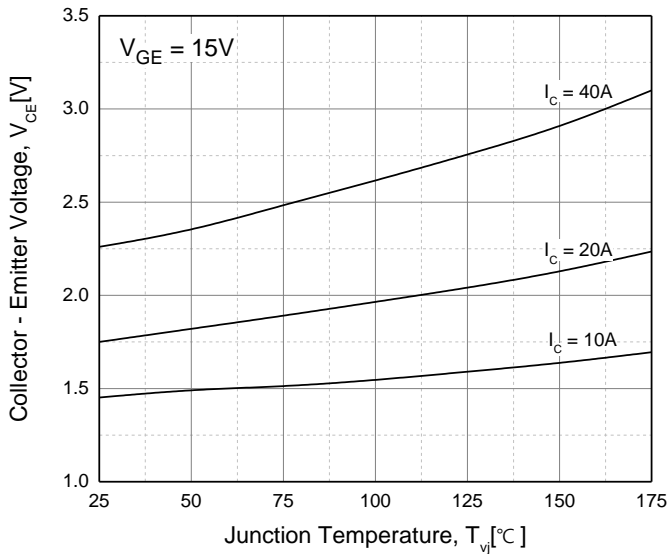
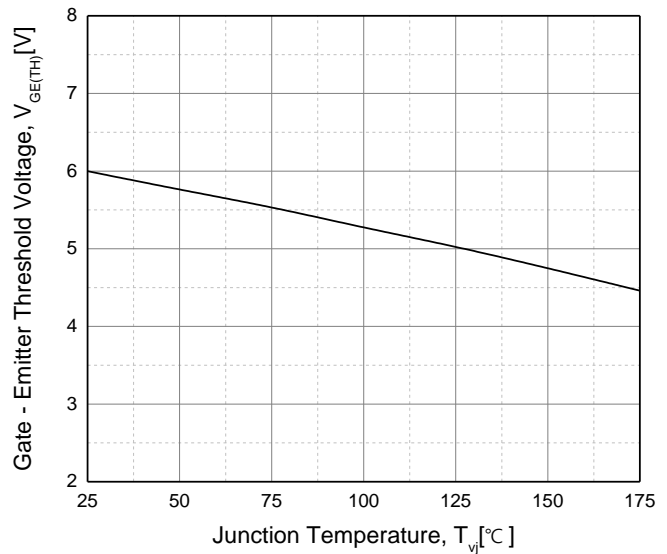


Fig. 4 IGBT Threshold Voltage vs. Junction Temperature



Device Characteristics

Fig. 5 IGBT Transfer Characteristic

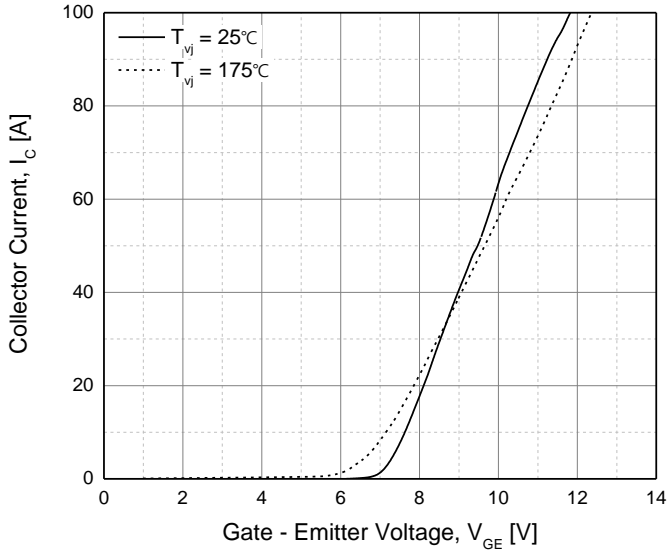


Fig. 6 IGBT Capacitance Characteristics

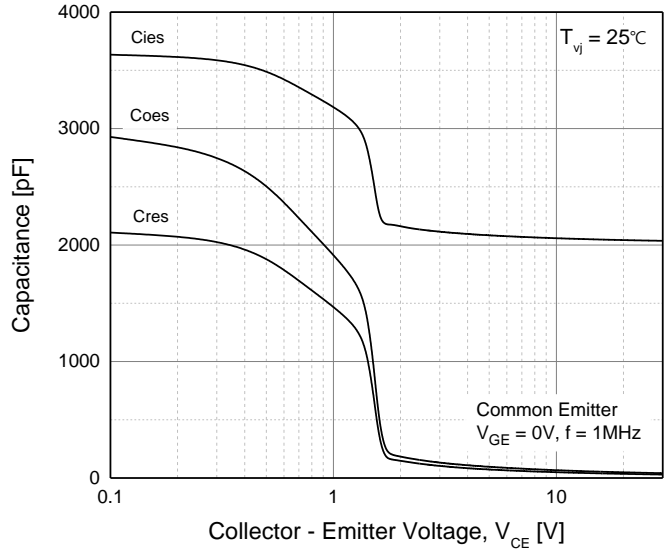


Fig. 7 Diode Conduction Characteristics

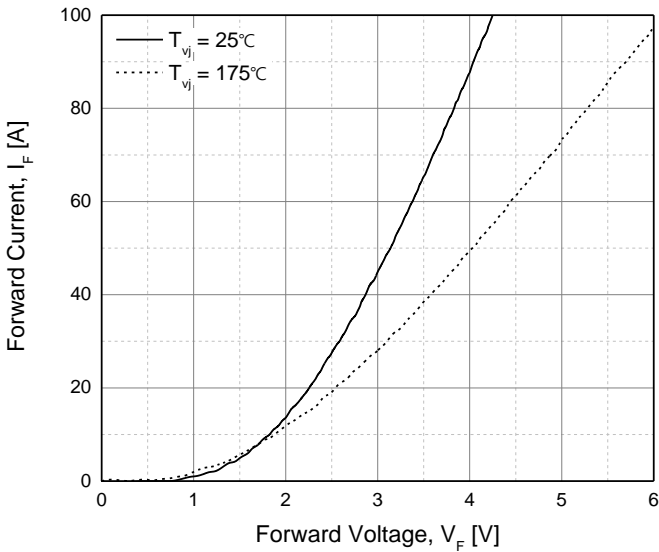
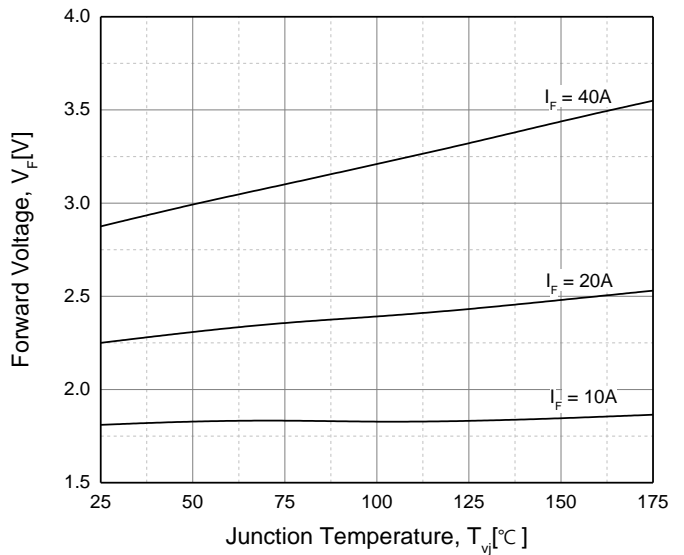


Fig. 8 Diode Forward Voltage vs. Junction Temperature



Device Characteristics

Fig. 9 Turn-off Time vs. Gate Resistor

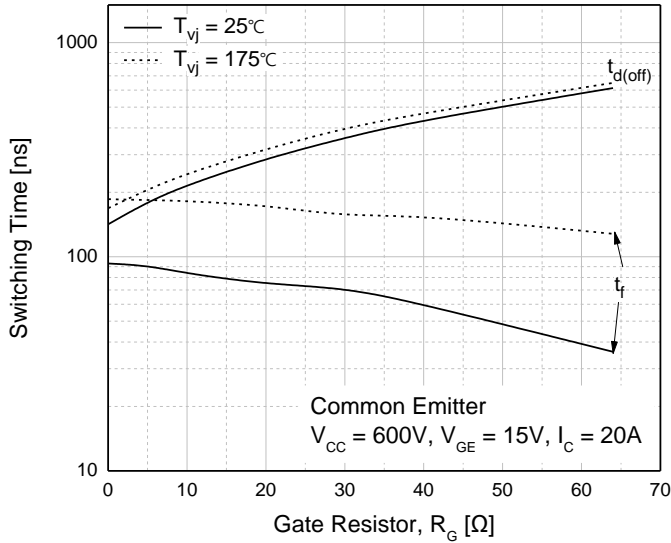


Fig. 10 Turn-off Time vs. Collector Current

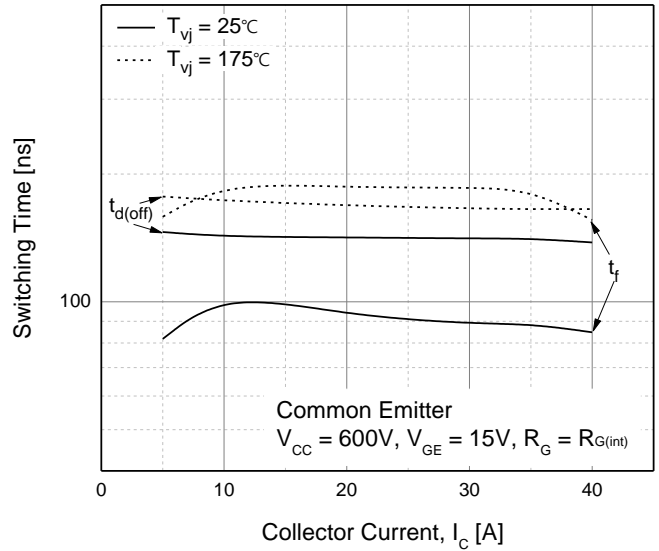


Fig. 11 Turn-off Loss vs. Gate Resistor

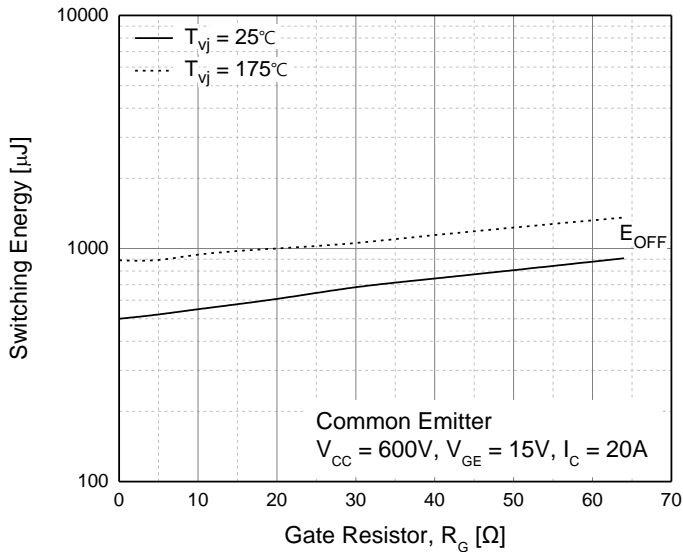
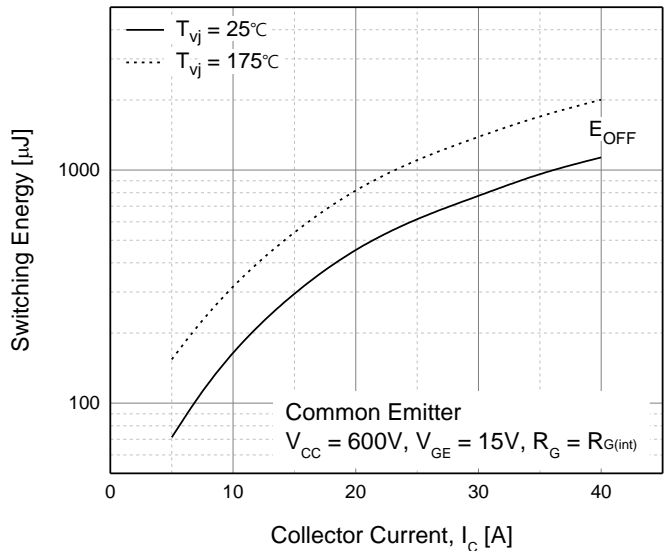


Fig. 12 Turn-off Loss vs. Collector Current



Device Characteristics

Fig. 13 Gate Charge Characteristics

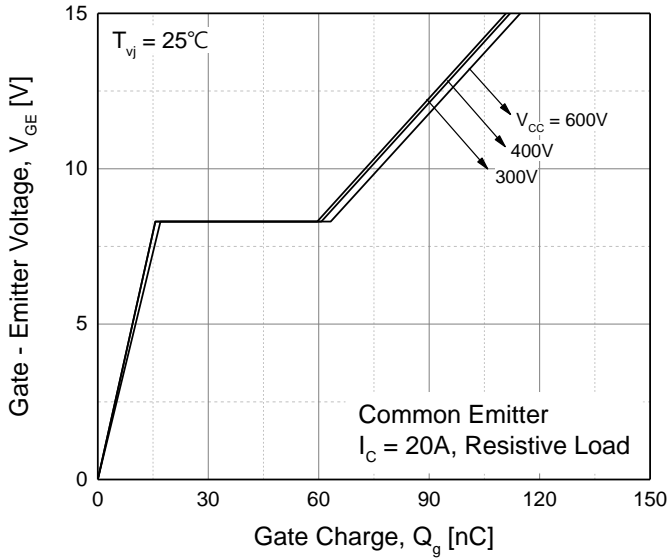


Fig. 14 Transient Thermal Impedance

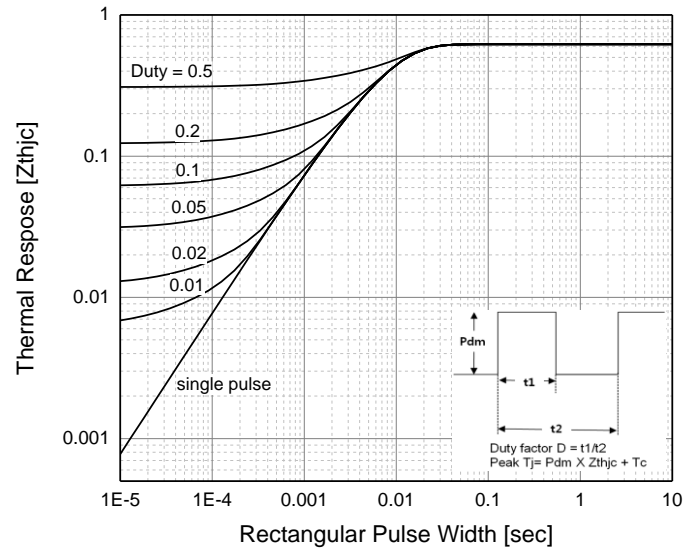


Fig. 15 Power Dissipation vs. Case Temperature

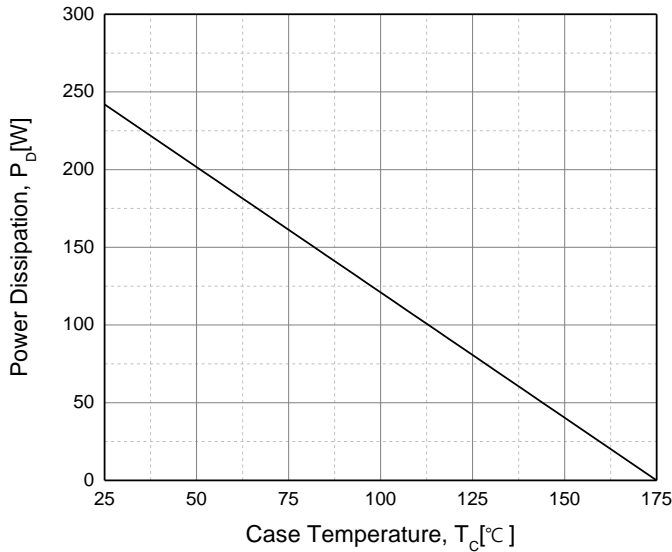
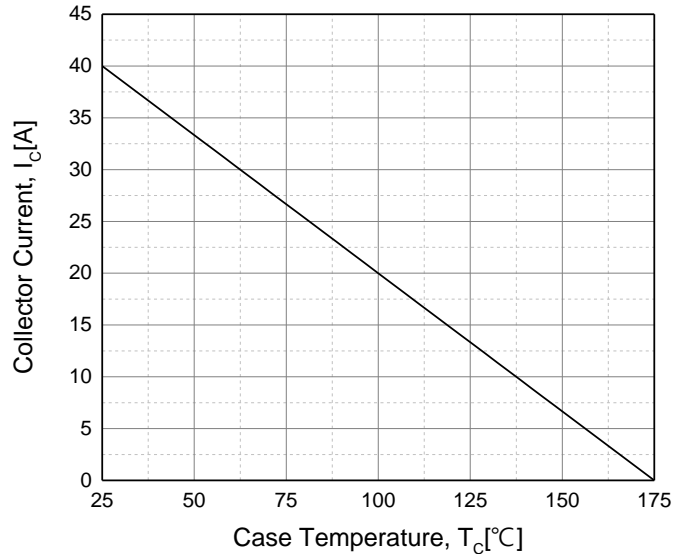


Fig. 16 Collector Current vs. Case Temperature



Device Characteristics

Fig. 17 SOA

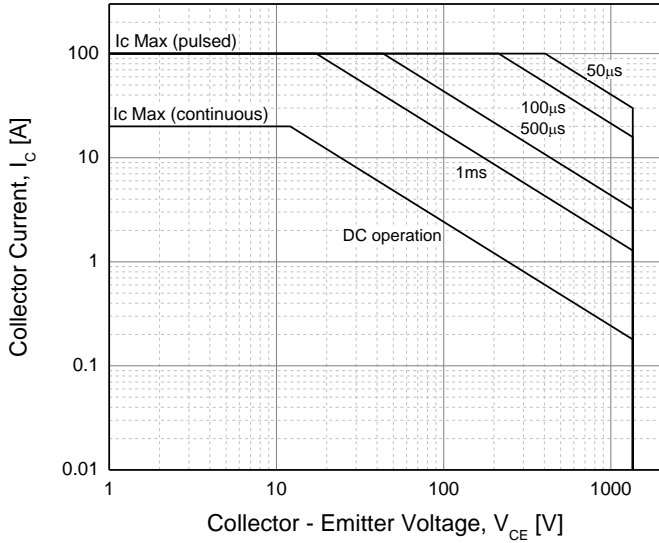


Fig. 18 RBSOA

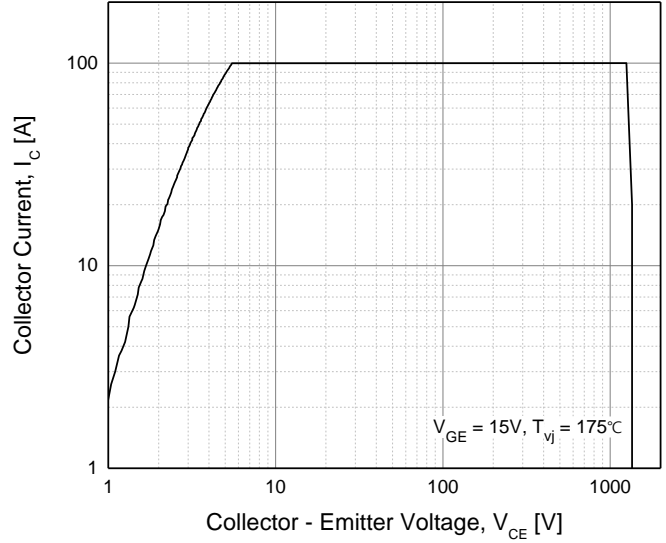


Fig. 19 Load Current vs. Frequency

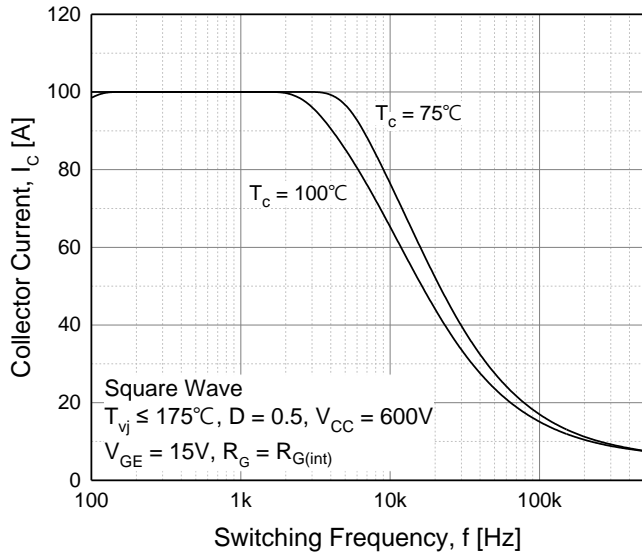
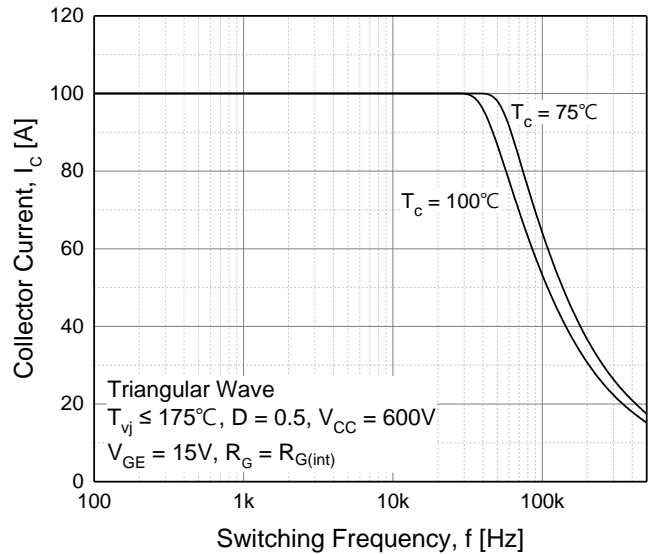
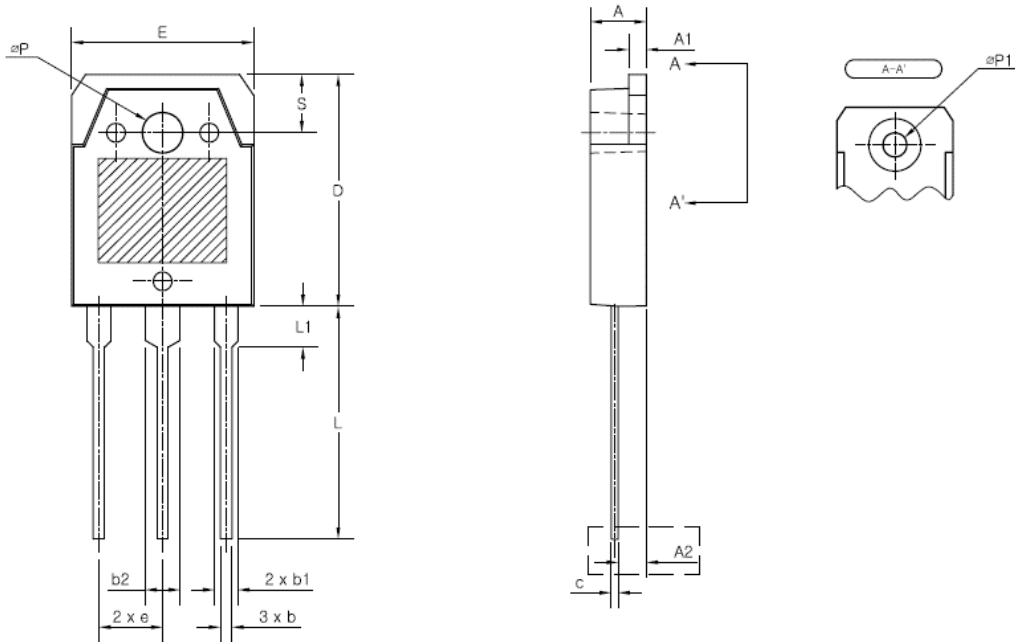


Fig. 20 Load Current vs. Frequency



TO-3PN MECHANICAL DATA



SYMBOL	mm		
	MIN	NOM	MAX
A	4.6	4.8	5
A1	1.45	1.5	1.65
A2	2.2	2.4	2.6
b	0.8	1	1.2
b1	2.8	3	3.2
b2	1.8	2	2.2
c	0.55	0.6	0.75
D	19.20	19.65	20.10
E	15.4	15.6	15.8
e	5.15	5.45	5.75
L	19.8	20	20.2
L1	3.3	3.5	3.7
ΦP	3.5		
ΦP1	3.2		
S	5		

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